#4 217/01

PATENT Atty. Dkt. AMAT/3083.P7/FET/FET/DV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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§ §

In re Application of:

Reginald Hunter

Serial No.:

UNKNOWN

Filed:

HEREWITH

For:

Method and Apparatus to Provide For Automated Process Verification and §

Hierarchical Substrate Examination

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Group Art Unit: UNKNOWN

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Examiner:

UNKNOWN



CERTIFICATION UNDER 37 CFR 1.10

I hereby certify that this correspondence is being deposited with the United States Postal Service on this date, O-6-200 , in an envelope as "Express Mail Post Office to Addressee," mailing label no. EL684620492US; addressed to: Assistant Commissioner for Patents, Box PATENT APPLICATION, Washington, D.C.

10-6-2000

Date

Name

INFORMATION DISCLOSURE STATEMENT

The applicants, and the attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While the information submitted in this Information Disclosure Statement may be material pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possibly material information as defined in 37 C.F.R. § 1.56(a) exists.

The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

Respectfully submitted,

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